

<u> 된</u>

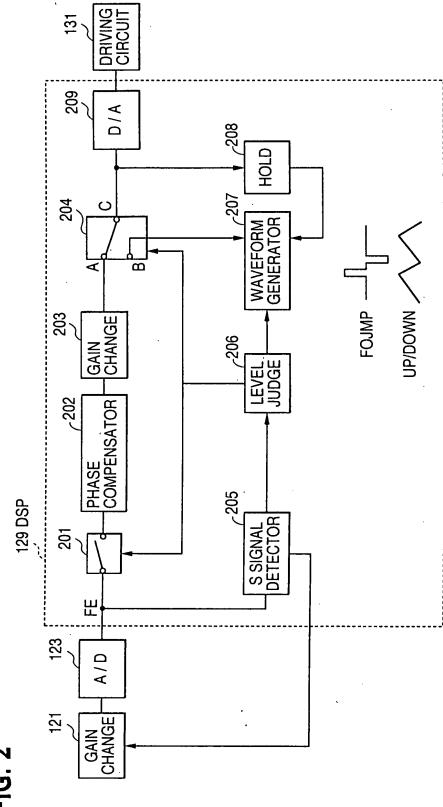
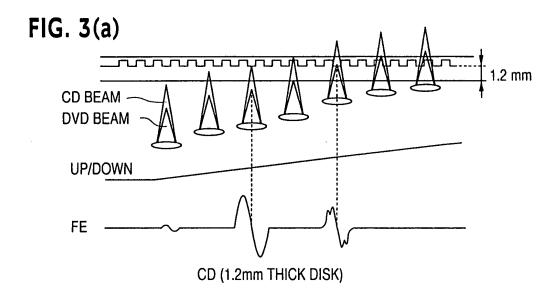
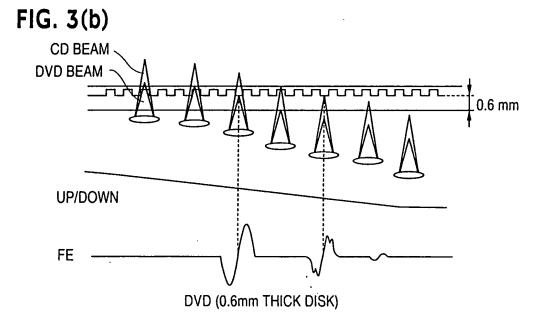
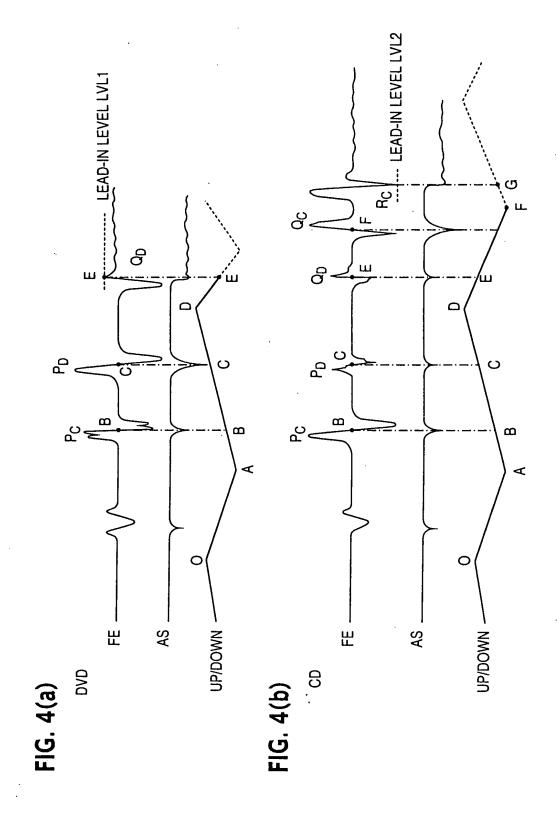
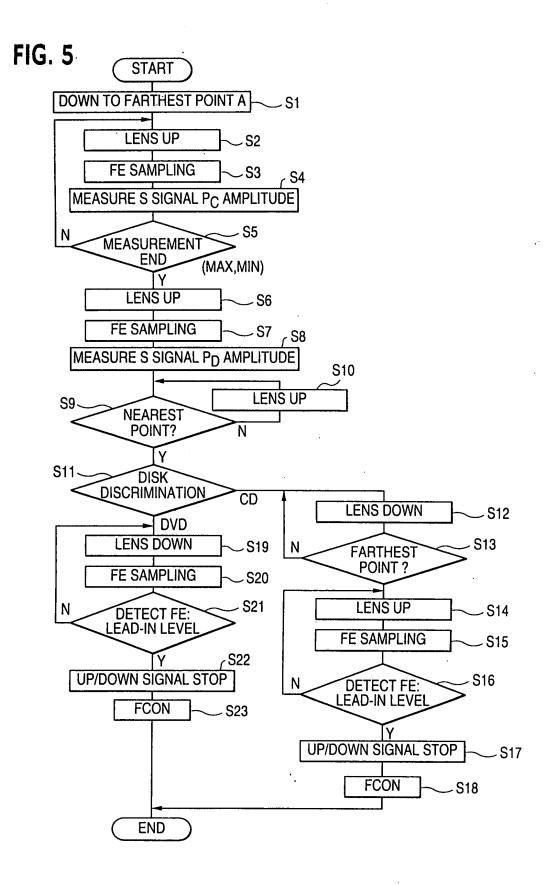


FIG. 2









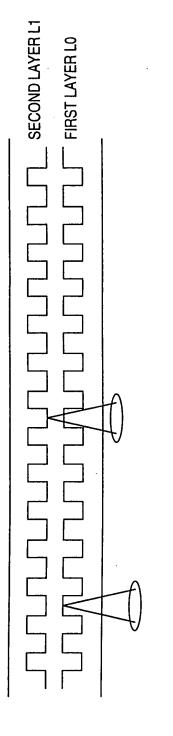


FIG. 6(b)

NOUNTINDUM NOUNTINDUM THIRD LAYER L2 NOUNTINDUM NOUNTINDUM SECOND LAYER L1 NOUNTINDUM NOUNTINDUM LIRST LAYER L0

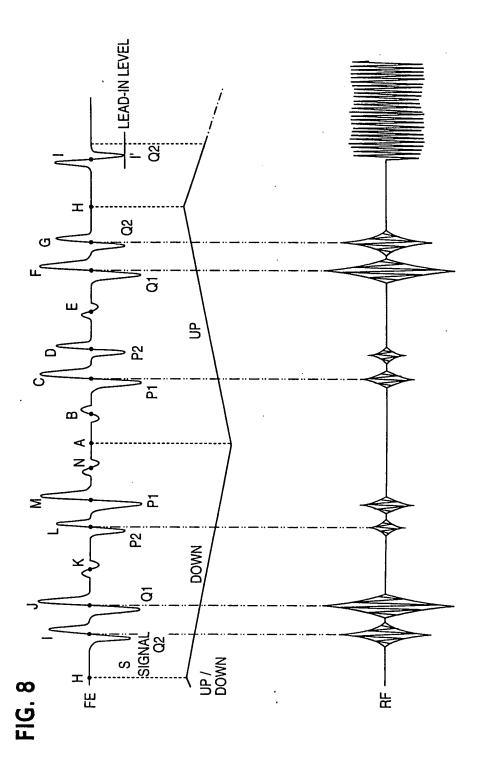
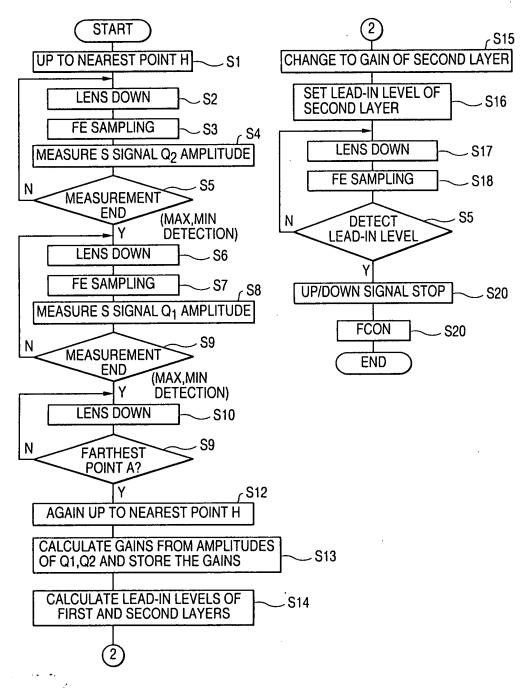


FIG. 9



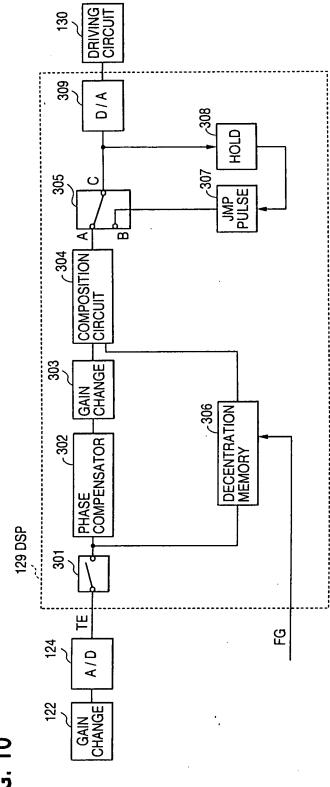


FIG. 10

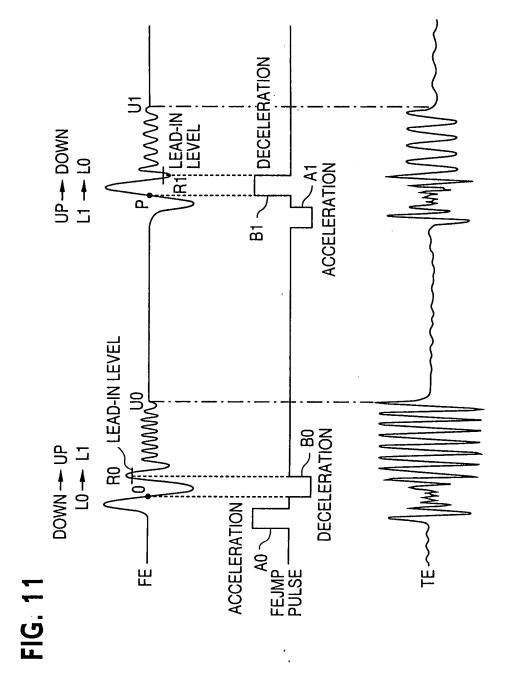
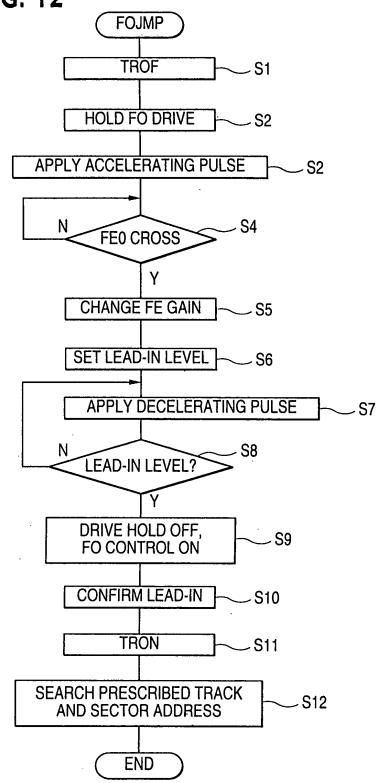


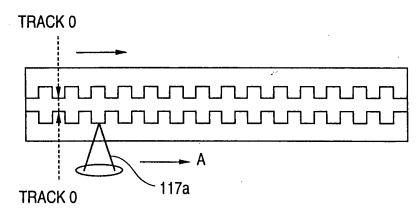
FIG. 12



PHASE COMPENSATOR ,-129 DSP 401 8 FEENV 出 128 A/D 127 F+ PH F- PH 4 ì PEAK HOLD PEAK HOLD 125b 116

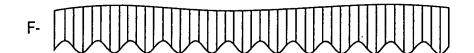
FIG. 13

FIG. 14





F+



F + PH

F - PH

FEENV

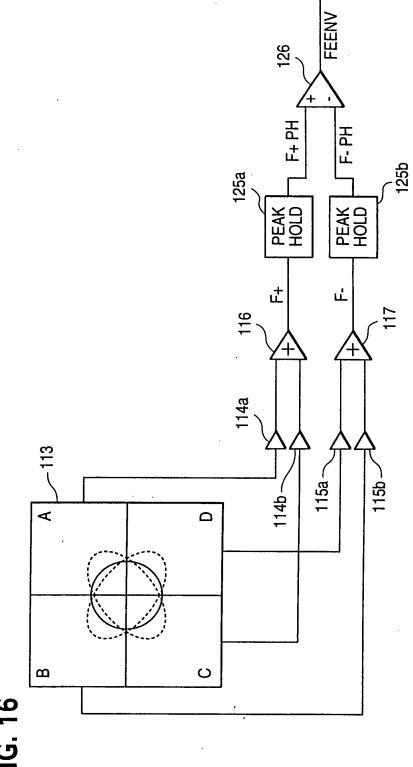


FIG. 17

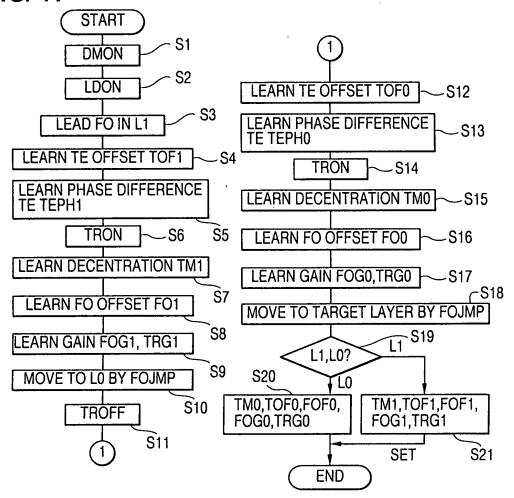
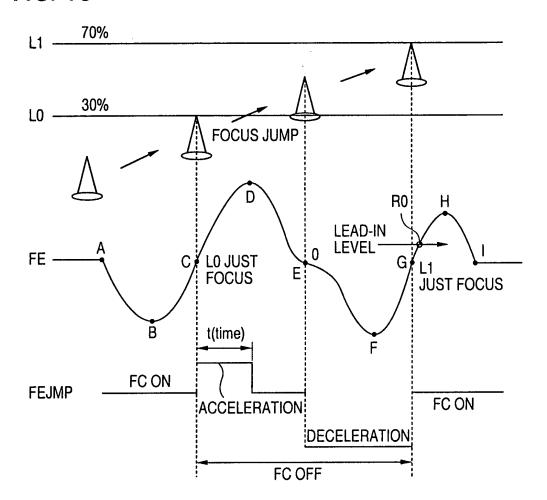
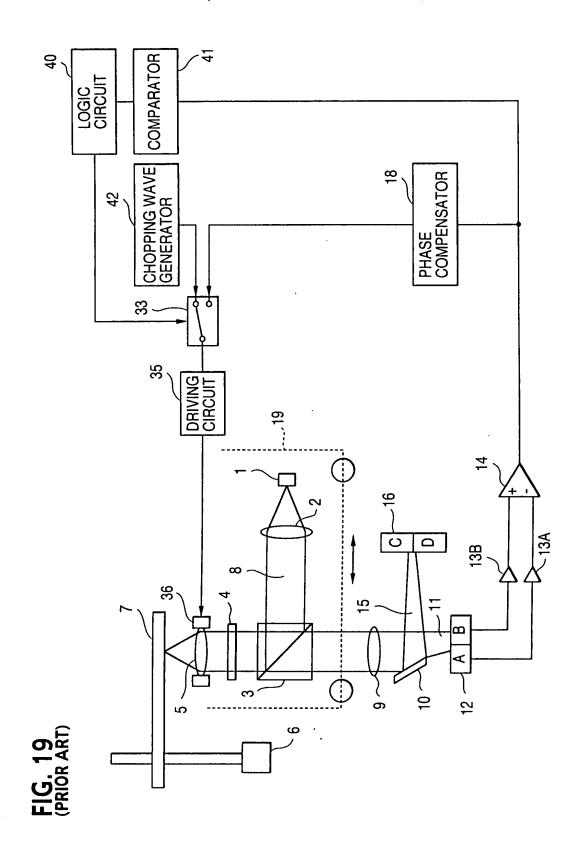
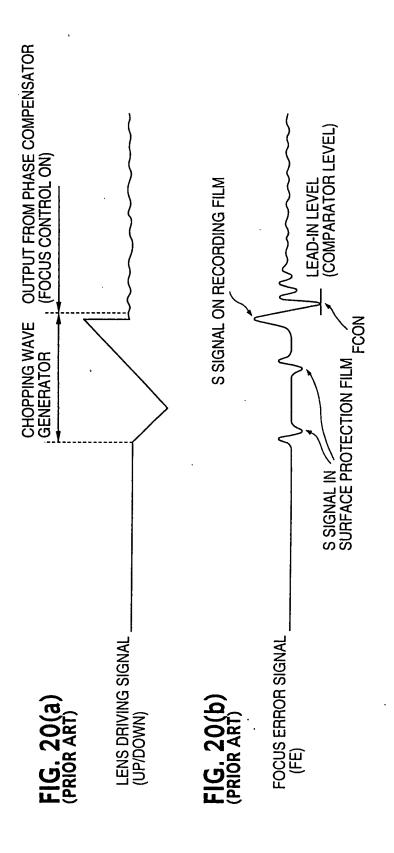


FIG. 18







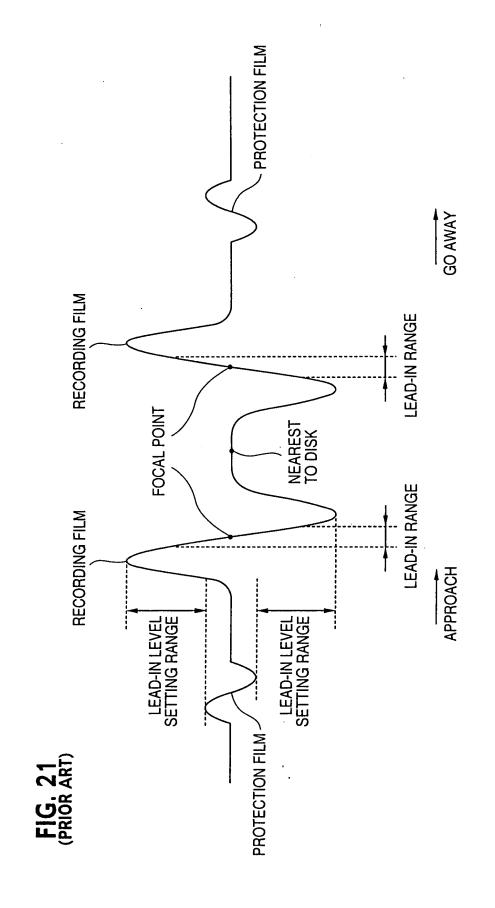
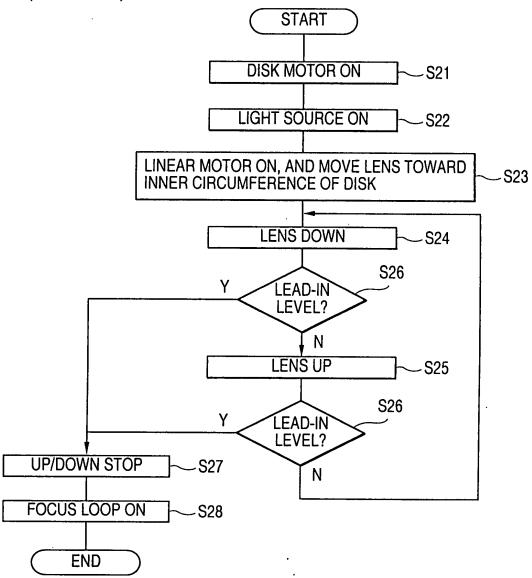
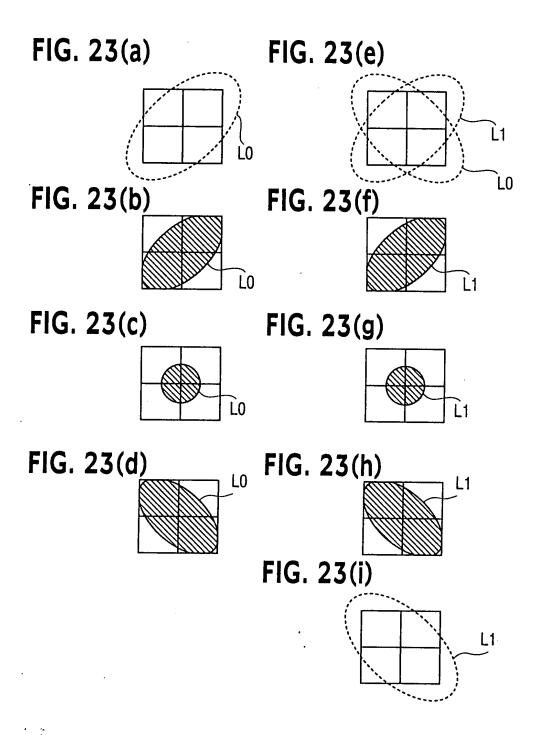


FIG. 22 (PRIOR ART)





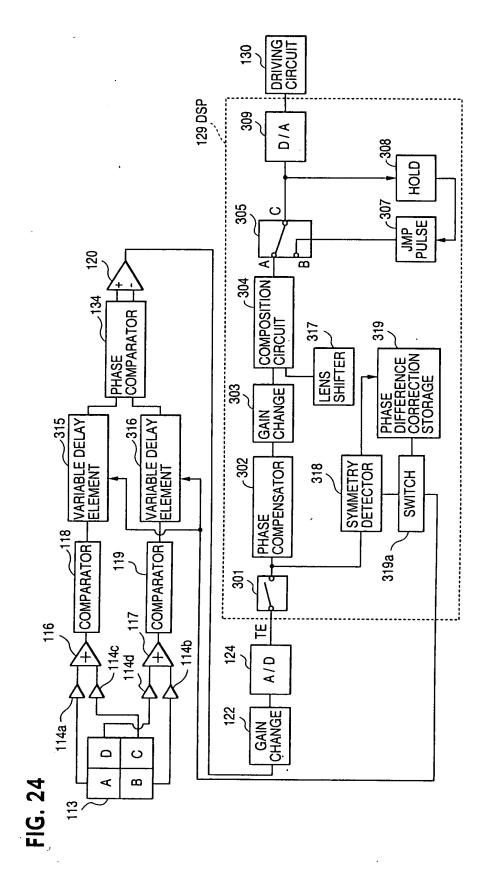
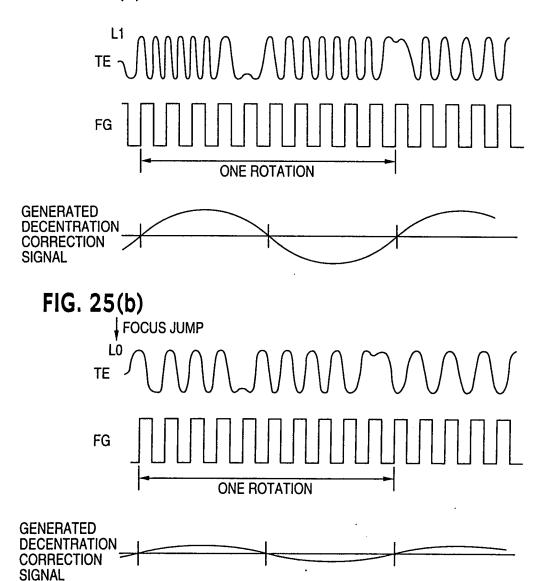


FIG. 25(a)



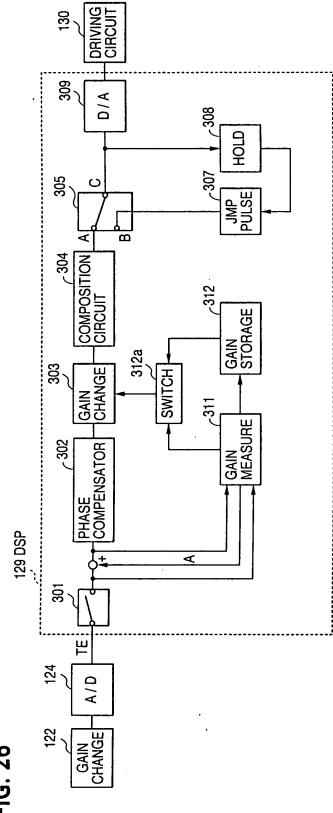


FIG. 26

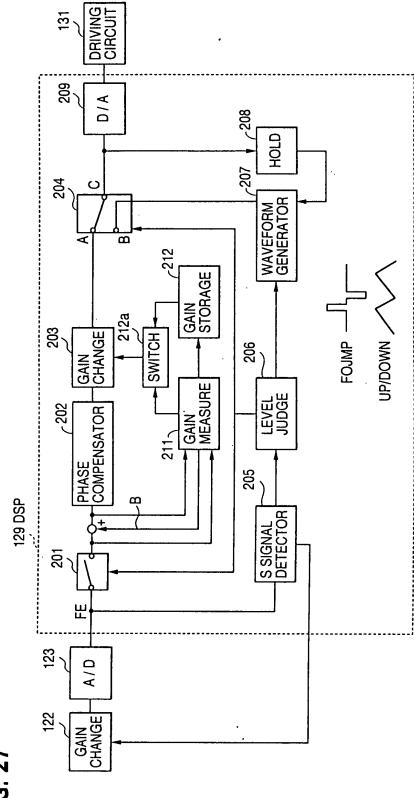


FIG. 27

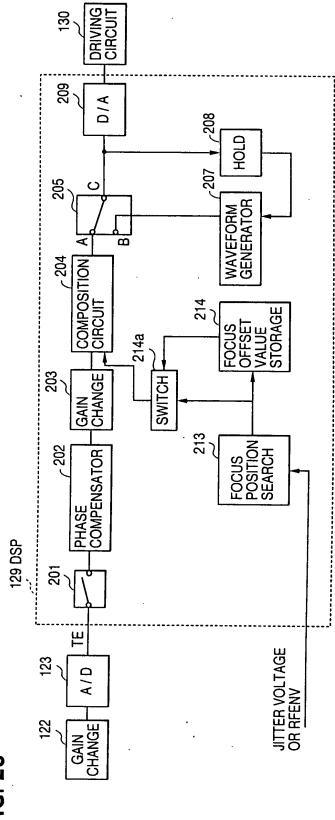


FIG. 28

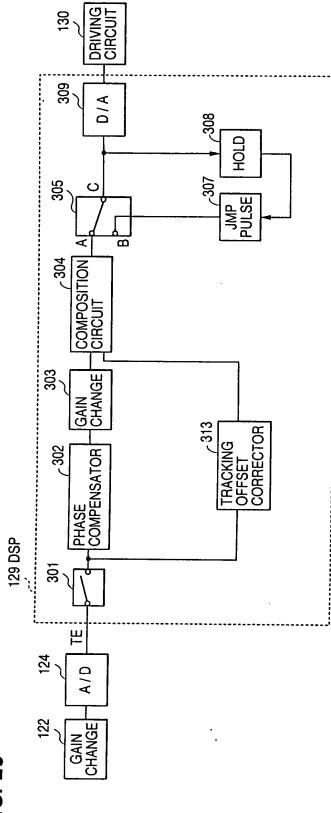


FIG. 29